

IN THE SPECIFICATION

Please amend the Title on page 1, lines 1-3, as follows:

INFORMATION RECORDING/REPRODUCING APPARATUS, ~~INFORMATION~~
~~RECORDING METHOD, AND INFORMATION RECORDING MEDIUM, WITH~~
~~RECORDED SUB-DATA INFORMATION REPRODUCING APPARATUS AND~~
INFORMATION RECORDING MET00480D

Please replace the section heading on page 7, at line 11, as follows:

~~DESCRIPTING~~ DESCRIPTION OF THE PREFERRED EMBODIMENTS

Please replace the paragraph at page 8, lines 6-13, with the following rewritten paragraph:

Q' That is, according to the disk original exposure apparatus 1, a spindle motor 4 drives to rotate the disk original 2 and FG signal FG a signal level of which rises at every predetermined rotational angle is outputted from an FG signal generating circuit held at a bottom portion thereof. A spindle servo circuit 5 drives the spindle motor 4 such that frequency of the FG signal FG becomes a predetermined frequency to thereby drive to rotate the disk original 2 under constant line speed.

Please replace the paragraph at page 8, lines 14-15, with the following rewritten paragraph:

Q2 A laser 6 is constituted by a gas laser or the like and emits laser beam L1 for exposing the disk original.

Please replace the paragraph at page 8, lines 16-22, with the following rewritten paragraph:

Q³ An optical modulator 7 is AOD (Acoustic Optical Deflector) constituted by an electro-acoustic optical element or the like for controlling an optical amount of the laser beam L1 emitted from the laser 6 by ON/OFF control in accordance with an optical modulation signal [[SD]] to thereby modulate the laser beam L1 by the optical modulation signal [[SD]] and emit the laser beam [[L1]] L2.

Please replace the paragraph at page 12, lines 8-20, with the following rewritten paragraph:

Q⁴ Here, according to the compact disk, bit error by the defect or the like is subjected to error correction processing by an error correction code added to the audio data. Further, when the size of the defect or the like is large and bit error which is difficult to correct is caused, the audio signal which is a result of reproduction is subjected to erasure or interpolation processing. In the case of the compact disk, when a defect having a predetermined length or more is caused and a frequency of such an erasure or interpolation processing becomes high, any user feels skeptical ~~strange feeling~~ with regard to the result of reproduction. That is, when such a defect having a predetermined length or more is caused, commercial value of the compact disk is significantly deteriorated.

Please replace the paragraph at page 12, line 21, through page 13, line 7, with the following rewritten paragraph:

Q⁵ That is, when a length of the defect deteriorating the commercial value in this way is designated by notation [[L1]] l1 and a length of the defect by which the audio data is difficult to reproduce correctly even by the error correction code is designated by notation [[L2]] l2,

Q5 the length of 16 (mm) allocated with 1 bit of the disk identifying code SB is constituted such that a length of the length of 16 (mm) removed of the length $[[L1]]$ 11 or $[[L2]]$ 12, respectively, is a length by which the disk identifying code SB can be reproduced correctly by a predetermined error rate or smaller in constitution of a recording/reproducing system of the disk identifying code SB, mentioned later.

Please replace the paragraph at page 13, lines 8-19, with the following rewritten paragraph:

Q6 Thereby, according to the disk original exposure apparatus 1, even when a defect is caused, in the case in which the audio data SA which is main data can be reproduced correctly by the error correction code and in the case in which the audio data SA constituting the main data can be reproduced correctly to a degree of maintaining commercial value as the compact disk, such that the disk identifying code SB can also be reproduced correctly, the disk identifying code SB is recorded to allocate to a long distance on the compact disk and only in the case in which a large defect to a degree of losing the commercial value as the compact disk is caused, the disk identifying code SB is constituted $[[to]]$ too difficult to reproduce.

Please replace the paragraph at page 13, line 20 through page 14, line 7, with the following rewritten paragraph:

Q7 Further, according to the disk original exposure apparatus 1, the relationship is established even with regard to a CD-ROM which is an application product of the compact disk, thereby, in place of forming the compact disk by recording the audio data SA, the disk original exposure apparatus 1 is applicable also in forming a CD-ROM. Further, in the case of a CD-ROM, when the bit error is difficult to correct even by the error correction function

Q7 of a CD-ROM intensified more than the compact disk, after all, in the data such as computer program is difficult to reproduce correctly, the commercial value is deteriorated and accordingly, only in such a case, the disk identifying code SB is difficult to reproduce.

Please replace the paragraph at page 15, lines 2-8, with the following rewritten paragraph:

Q8 Fig. 1 is a block diagram showing a detailed constitution of the additional modulating circuit 14. The additional modulating circuit 14 inputs the EFM signal EFM to a PLL circuit, not illustrated, and reproduces channel clock of the EFM signal EFM by the PLL circuit. The additional modulating circuit 14 supplies respective circuit blocks with the channel clock as a reference of processing the EFM signal EFM.

Please replace the paragraph at page 16, lines 14-24, with the following rewritten paragraph:

Q9 An M series calculating circuit 24 is constituted by a number of continuously connected flip ~~flop~~ flops and exclusive OR and sets the initial values outputted from the initial value generating circuit 23 to these flip ~~flop~~ flops by timings of the synchronization detecting signal SY. The M series calculating circuit 24 successively transfers content held by these flip ~~flop~~ flops in synchronism with the channel clock of the EFM signal EFM to thereby generate and output the M series signal MA. Here, the M series signal MA is a random number of M series and is a signal in which logic 1 and logic 0 emerge randomly at an equal probability.
